

CPU
Atmel ARM
Cortex-A5
ATSAMA5D36
528MHz

Memory
DDR2
256MBytes

3 x USB
Ethernet RX/TX
SD/MMC
LCD
CSI
i2S
4 x UART
2 x SPI
I2C
2 x CAN
2 x PWM
ADC 9 inputs
Soft modem
GPIO

SLC NAND Flash
256 MBytes

Ethernet PHY
100/10M

Power system

X1		X1		
41	SD1_D1	I2C1_SDA	42	I2C1_SDA
43	SD1_D2	I2C1_SCL	44	I2C1_SCL
45	SD1_D3	I2C2_SDA	46	PE18
47	GND	I2C2_SCL	48	PE19
49	RESERVED	PWM2	50	PWM2
51	RESERVED	PWM1	52	PWM1
53	RESERVED	GND	54	GND
55	RESERVED	CS1_PCK	56	CS1_PCK
57	RESERVED	CS1_HS	58	CS1_HS
59	RESERVED	CS1_VS	60	CS1_VS
61	RESERVED	CS1_D7	62	CS1_D7
63	RESERVED	CS1_D6	64	CS1_D6
65	RESERVED	CS1_D5	66	CS1_D5
67	RESERVED	CS1_D4	68	CS1_D4
69	RESERVED	CS1_D3	70	CS1_D3
71	RESERVED	CS1_D2	72	CS1_D2
73	RESERVED	CS1_D1	74	CS1_D1
75	RESERVED	CS1_D0	76	CS1_D0
77	RESERVED	I2S_TXFS	78	AUD_TXFS
79	RESERVED	I2S_RXD	80	AUD_RXD
81	RESERVED	I2S_TXD	82	AUD_TXD
83	RESERVED	I2S_TXC	84	AUD_TXC
85	RESERVED	I2S_MCLK	86	AUD_MCLK
87	GND	GND	88	GND
89	SATA_TXP	BUS_BCLK	90	PB2
91	SATA_TXN	BUS_CS1	92	PB3
93	SATA_RKN	BUS_CS0	94	PB4
95	SATA_RXP	BUS_DE	96	PB5
97	GND	BUS_WAIT	98	PB6
99	HDMI_D0N	BUS_Rw	100	PB7
101	HDMI_D0P	BUS_LBA	102	PB8
103	HDMI_D0	BUS_EB1	104	PB9
105	HDMI_P0	BUS_EB0	106	PB10
107	HDMI_N1	BUS_DA15	108	PB11
109	HDMI_P1	BUS_DA14	110	PB12
111	HDMI_N2	BUS_DA13	112	PB13
113	HDMI_P2	BUS_DA12	114	PB17
115	GND	BUS_DA11	116	PB18
117	LVDS_0_TXN	BUS_DA10	118	
119	LVDS_0_TXP	BUS_DA9	120	
121	LVDS_1_TXN	BUS_DA8	122	
123	LVDS_1_TXP	BUS_DA7	124	
125	LVDS_2_TXN	BUS_DA6	126	
127	LVDS_2_TXP	BUS_DA5	128	
129	LVDS_CLKN	BUS_DA4	130	
131	LVDS_CLKP	BUS_DA3	132	
133	LVDS_3_TXN	BUS_DA2	134	
135	LVDS_3_TXP	BUS_DA1	136	
137	GND	BUS_DA0	138	
139	USB4_N	GND	140	GND
141	USB4_P	LCD_DE	142	LCD_DE
143	USB3_N	LCD_VS	144	LCD_VS
145	USB3_P	LCD_HS	146	LCD_HS
147	USB2_N	LCD_B7	148	LCD_B7
149	USB2_P	LCD_B6	150	LCD_B6
151	USB1_OTG_N	LCD_B5	152	LCD_B5
153	USB1_OTG_P	LCD_B4	154	LCD_B4
155	USB1_OTG_VBUS	LCD_B3	156	LCD_B3
157	USB1_OTG_ID	LCD_B2	158	LCD_B2
159	3.3V	3.3V_OUT	160	LCD_B1
161	ETH_LED1	LCD_B0	162	LCD_B0
163	ETH_LED2	LCD_G7	164	LCD_G7
165	ETH_CT	LCD_G6	166	LCD_G6
167	ETH_TRO1P/TXN	LCD_G5	168	LCD_G5
169	ETH_TRO1N/TXN	LCD_G4	170	LCD_G4
171	ETH_TRO2P/RXN	LCD_G3	172	LCD_G3
173	ETH_TRO2N/RXN	LCD_G2	174	LCD_G2
175	ETH_TRO3P	LCD_G1	176	LCD_G1
177	ETH_TRO3N	LCD_G0	178	LCD_G0
179	ETH_TRO4P	LCD_R7	180	LCD_R7
181	ETH_TRO4N	LCD_R6	182	LCD_R6
183	GND	LCD_R5	184	LCD_R5
185	PCIE_TXP	LCD_R4	186	LCD_R4
187	PCIE_TXN	LCD_R3	188	LCD_R3
189	PCIE_RXP	LCD_R2	190	LCD_R2
191	PCIE_RXN	LCD_R1	192	LCD_R1
193	PCIE_SREFP	LCD_R0	194	LCD_R0
195	PCIE_SREFN	LCD_CLK	196	LCD_CLK
197	GND	GND	198	GND
199	3.3V	3.3V_OUT	200	GND

X1		X1	
5V	5V	5V	5V
5V	5V	5V	5V
5V	5V	5V	5V
GND	GND	GND	GND
SPI2_CLK	SPI2_CLK	SPI1_CLK	SPI1_CLK
SPI2_CS	SPI2_CS	SPI1_CS	SPI1_CS
SPI2_MOSI	SPI2_MOSI	SPI1_MOSI	SPI1_MOSI
SPI2_MISO	SPI2_MISO	SPI1_MISO	SPI1_MISO
RESET	RESET	UART1_TX_CON	UART1_TX_CON
VBAT	VBAT	UART1_RX_CON	UART1_RX_CON
SD1_D0	SD1_D0	UART2_TX	UART2_TX
SD1_D1	SD1_D1	UART2_RX	UART2_RX
SD1_D2	SD1_D2	UART3_TX	UART3_TX
SD1_D3	SD1_D3	UART3_RX	UART3_RX
SD1_CMD	SD1_CMD	UART4_TX	UART4_TX
SD1_CLK	SD1_CLK	UART4_RX	UART4_RX
3.3V	3.3V_OUT	CAN1_TX	CAN1_TX
3.3V	3.3V_OUT	CAN1_RX	CAN1_RX
3.3V	3.3V_OUT	CAN2_TX	CAN2_TX
3.3V	3.3V_OUT	CAN2_RX	CAN2_RX

SK-ATSAMA5D3-SODIMM	
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V1B	