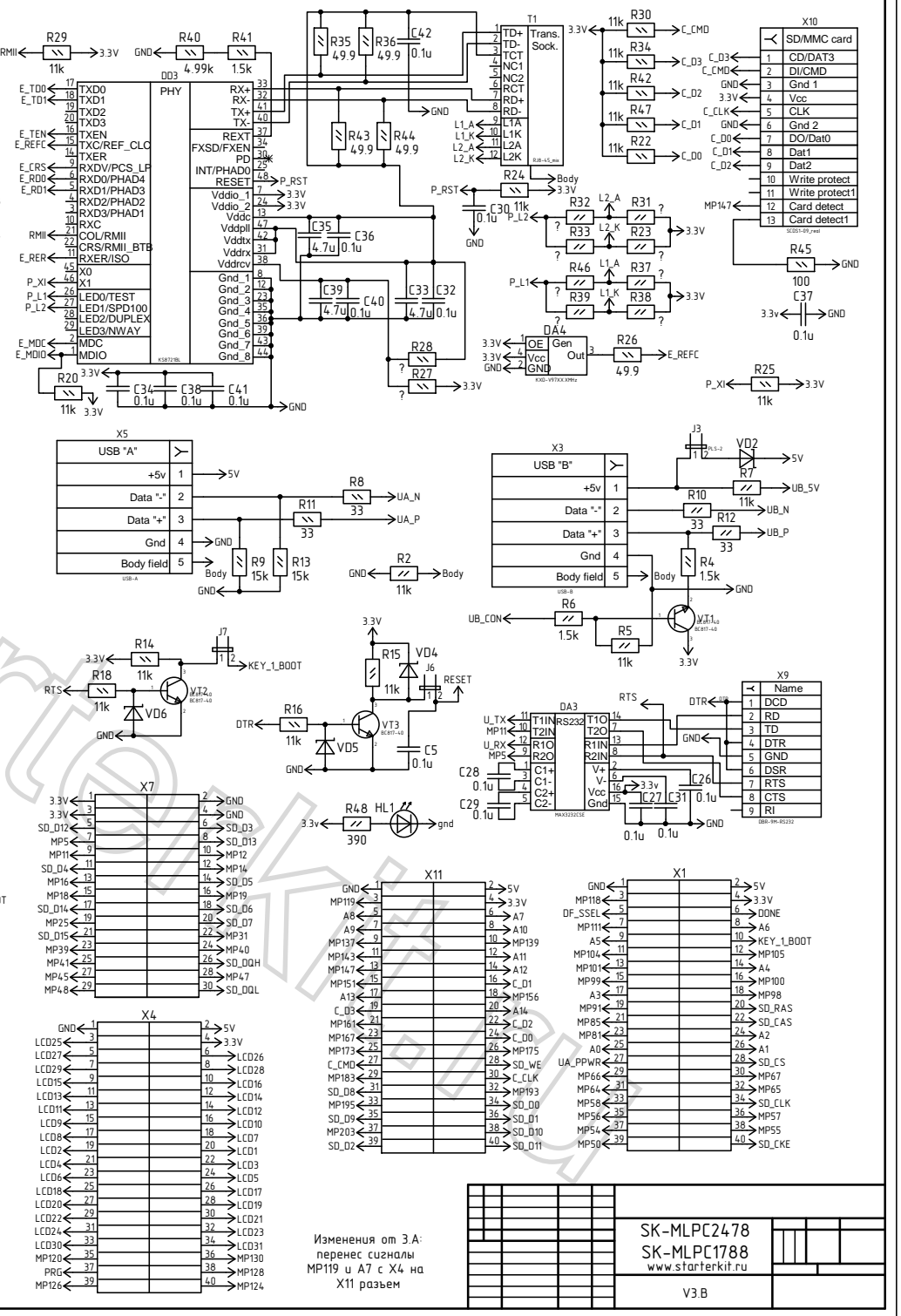


DD2 ARM7

- P0[0]RD1/TXD3/SDA1
- P0[1]TD1/RXD3/SCL1
- P0[2]TDO
- P0[3]RXD0
- P0[4]I2SRX_CLK/L0/RD2/CAP2[0]
- P0[5]I2SRX_WS/L1/TD2/CAP2[1]
- P0[6]I2SRX_SDA/L8/SSEL1/MAT2[0]
- P0[7]I2STX_CLK/L9/CSK1/MAT2[1]
- P0[8]I2STX_WS/L16/MISO1/MAT2[2]
- P0[9]I2STX_SDA/L17/MOSI1/MAT2[3]
- P0[10]TXD2/SDA2/MAT3[0]
- P0[11]RXD2/SCL2/MAT3[1]
- P0[12]USB_PPWR2/MS01/CA0[6]
- P0[13]USB_UP_LED2/MOSI1/AD0[6]
- P0[14]USB_HSTEN2/USB_CONNECT2/SSEL1
- P0[15]TXD1/CSCK/SCK
- P0[16]RXD1/SSEL0/SCK
- P0[17]CTS1/MOSI0/SSEL0
- P0[18]JDD31/MOSI0/SSEL0
- P0[19]DSR1/MCICLK/SDA1
- P0[20]DTR1/MCICMD/SCL1
- P0[21]R11/MCIPWR/RD1
- P0[22]R15/S0/MS0/SDA1
- P0[23]AD[0]I2SRX_WS/CAP3[1]
- P0[24]AD[0]I2SRX_SDA/TX3
- P0[25]AD[0]I2SRX_SDA/TX3
- P0[26]AD[0]J3/AOUT/RXD3
- P0[27]SDA0
- P0[28]SCL0
- P0[29]USB_DP1
- P0[30]USB_DN1
- P0[31]USB_DP2
- P0[32]USB_DN2
- P1[0]ENET_TXD0
- P1[1]ENET_TXD1
- P1[2]ENET_TXD2/MCICLK/PWM0[1]
- P1[3]ENET_TXD3/MCICMD/PWM0[2]
- P1[4]ENET_TXD4
- P1[5]ENET_TXD5/ERMCMD/PWM0[3]
- P1[6]ENET_TXD6/CLKMCDAT/PWM0[4]
- P1[7]ENET_TXD7/ENET_CRS
- P1[8]ENET_CRS_DV/ENET_CRS
- P1[9]ENET_RXD0
- P1[10]ENET_RXD1
- P1[11]ENET_RXD2/MCICMD/PWM1[6]
- P1[12]ENET_RXD3/MCICMD/PWM1[7]
- P1[13]ENET_RXD4
- P1[14]ENET_RXD5
- P1[15]ENET_REF_CLK/ENET_RX_CLK
- P1[16]ENET_MDIO
- P1[17]ENET_MDIO
- P1[18]USB_UP_LED1/PWM1[1]/CAP1[0]
- P1[19]USB_TX_E1/USB_PPWR1/CAP1[1]
- P1[20]USB_TX_DP1/L6/L10/PWM1[2]/SCK0
- P1[21]USB_TX_DM1/L17/L19/PWM1[5]/SSEL0
- P1[22]USB_RXCV1/L8/L12/USB_PPWR1/MAT1[0]
- P1[23]USB_RX_DP1/L9/L13/PWM1[4]/MISO0
- P1[24]USB_RX_DM1/L10/L14/PWM1[5]/MOSI0
- P1[25]USB_LS1/L1/L15/USB_HSTEN1/MAT1[1]
- P1[26]USB_SSPND/L12/L20/PWM1[6]/CA0[0]
- P1[27]USB_INT/L13/L21/USB_OVCR2/CAP1[1]
- P1[28]USB_SCL1/L1/L12/PCAP1[0]/MAT0[1]
- P1[29]USB_SDA1/L15/L23/PCAP1[1]/MAT0[1]
- P1[30]USB_PWRD2/VBUS/AD0[4]
- P1[31]USB_OVCR2/SCK1/AD0[5]
- P2[0]PWM1[1]/TXD1/TRACECLK/LDPPWR
- P2[1]PWM1[2]/RXD1/PIPESTAT/LCDCLK
- P2[2]PWM1[3]/CTS1/PIPESTAT1/LCDCLK
- P2[3]PWM1[4]/DCC1/PIPESTAT2/LCDCLK
- P2[4]PWM1[5]/DSR1/TRACESYNC/LCDENAB/LCD
- P2[5]PWM1[6]/DTR1/TRACEPKT1/L1/L15
- P2[6]PCAP1[0]/R11/TRACEPKT2/L1/L15
- P2[7]RD2/R15/TRACEPKT2/L1/L15
- P2[8]RD2/TXD2/TRACEPKT3/L2/L16
- P2[9]USB_CONNECT1/RXD2/XTIN0/L3/L17
- P2[10]EINT0
- P2[11]EINT1/LCDCLK/MCICMD/1/I2STX_CLK
- P2[12]EINT2/L4/L13/L9/L18/MCICMD/2/I2STX_WS
- P2[13]EINT3/L5/L9/L19/MCICMD/3/I2STX_SDA
- P2[14]CS2/CAP2[0]/SDA1
- P2[15]CS3/CAP2[1]/SCL1
- P2[16]CAS
- P2[17]RAS
- P2[18]CAS
- P2[19]CLKOUT0
- P2[20]CLKOUT1
- P2[21]DVC50
- P2[22]DVC52/CAP3[0]/SCK0
- P2[23]DVC53/CAP3[1]/SSEL0
- P2[24]CKEOUT0
- P2[25]CKEOUT1
- P2[26]CKEOUT2/MAT3[0]/MOSI0
- P2[27]CKEOUT3/MAT3[1]/MOSI0
- P2[28]DQMOUT0
- P2[29]DQMOUT1
- P2[30]DQMOUT2/MAT3[2]/SDA2
- P2[31]DQMOUT3/MAT3[3]/SCL2



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