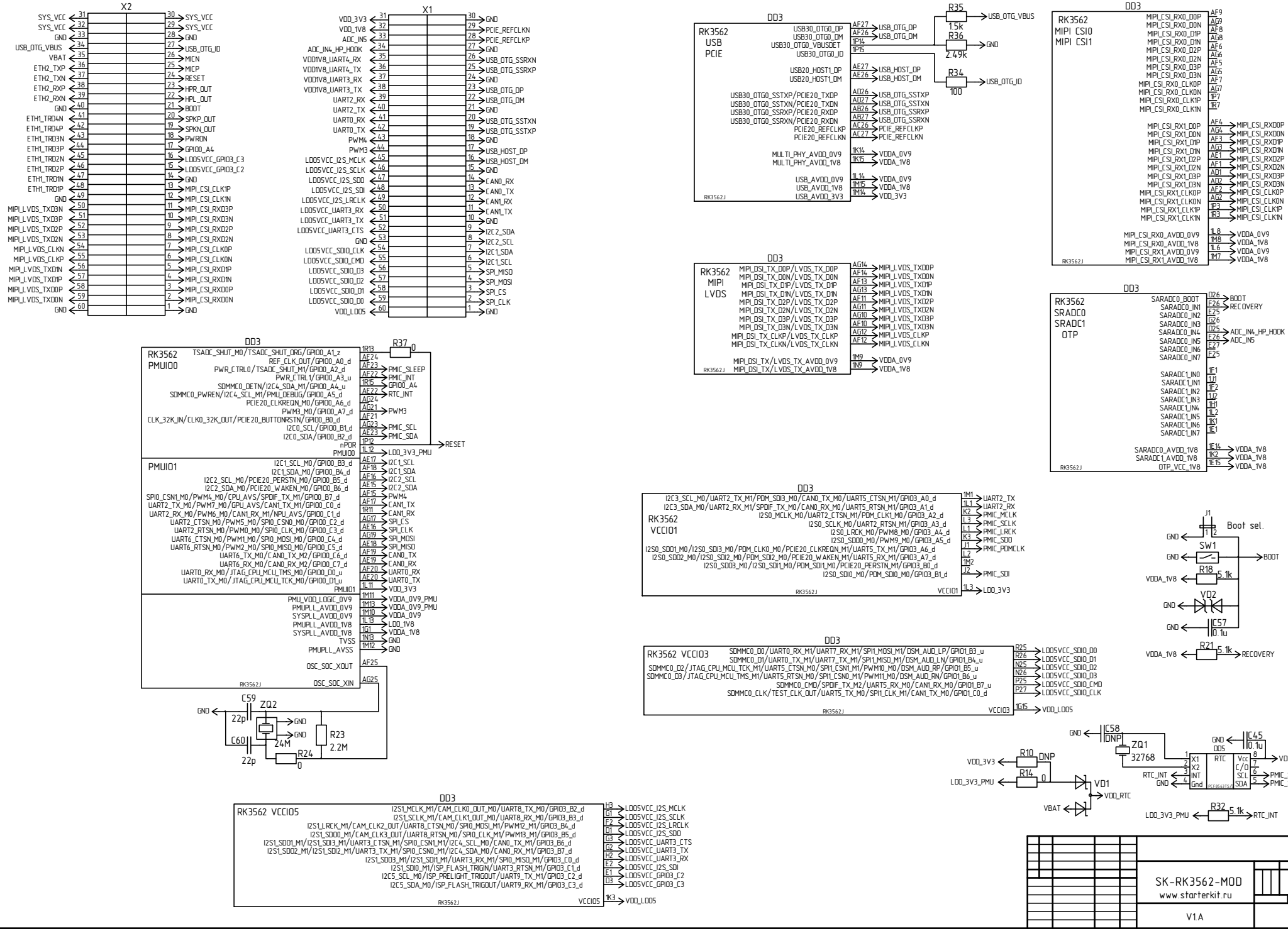
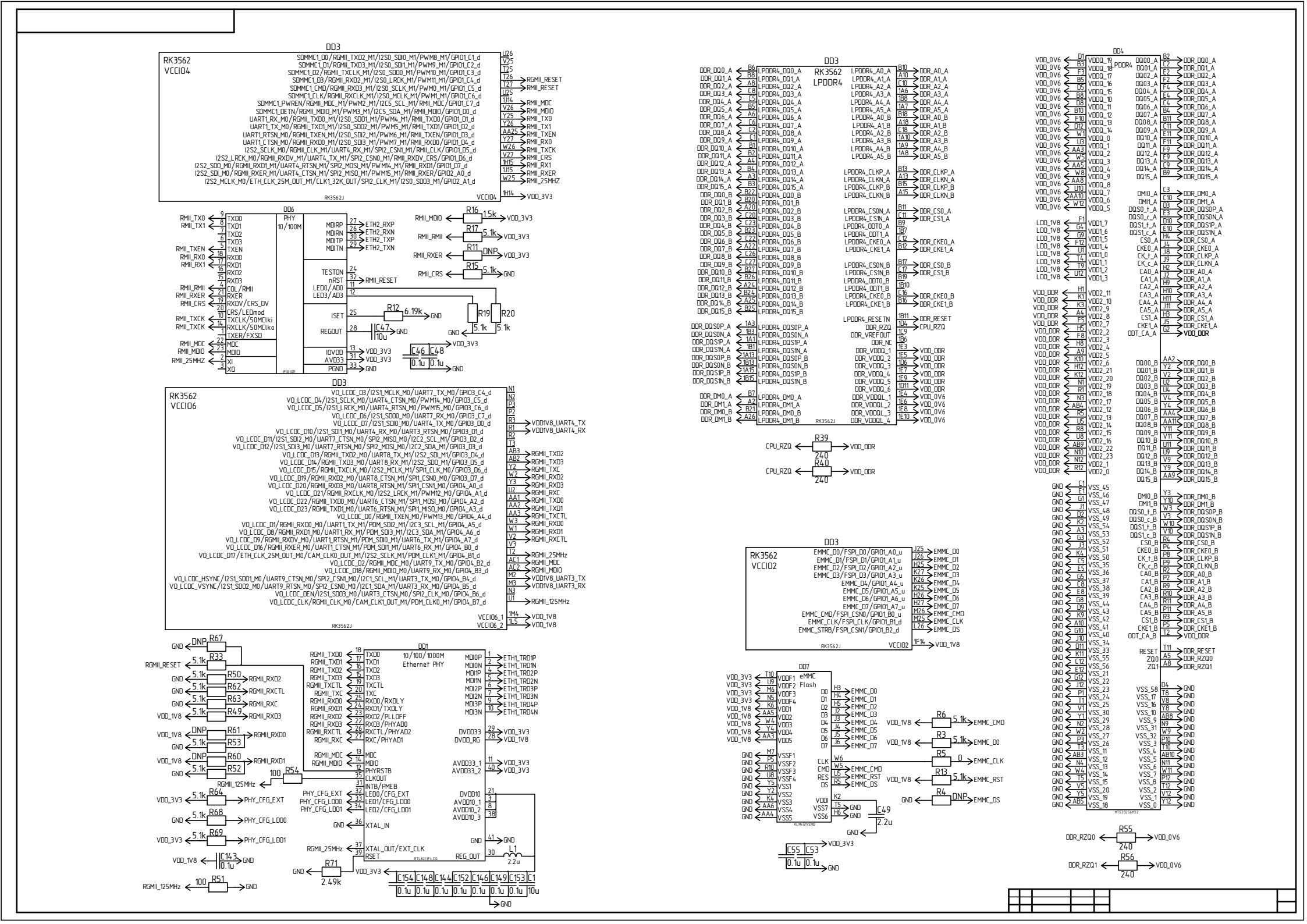

X2
SYS_VCC
SYS_VCC
GND
USB_OTG_VBUS
VBAT
ETH2_TXP
ETH2_TXN
ETH2_RXP
ETH2_RXN
GND
ETH1_TRD4N
ETH1_TRD4P
ETH1_TRD3N
ETH1_TRD3P
ETH1_TRD2N
ETH1_TRD2P
ETH1_TRD1N
ETH1_TRD1P
GND
MIPI_LVDS_TXD3N
MIPI_LVDS_TXD3P
MIPI_LVDS_TXD2P
MIPI_LVDS_TXD2N
MIPI_LVDS_CLKN
MIPI_LVDS_CLKP
MIPI_LVDS_TXD1N
MIPI_LVDS_TXD1P
MIPI_LVDS_TXD0P
MIPI_LVDS_TXD0N
GND
MICN
MICP
RESET
HPR_OUT
HPL_OUT
BOOT
SPKP_OUT
SPKN_OUT
PWRON
GPIO0_A4
LDO5VCC_GPIO3_C3
LDO5VCC_GPIO3_C2
MIPI_CSI_CLK1P
MIPI_CSI_CLK1N
MIPI_CSI_RXD3P
MIPI_CSI_RXD3N
MIPI_CSI_RXD2P
MIPI_CSI_RXD2N
MIPI_CSI_CLK0P
MIPI_CSI_CLK0N
MIPI_CSI_RXD1P
MIPI_CSI_RXD1N
MIPI_CSI_RXD0P
MIPI_CSI_RXD0N
X1
VDD_3V3
VDD_1V8
ADC_IN5
ADC_IN4_HP_HOOK
VDD1V8_UART4_RX
VDD1V8_UART4_TX
VDD1V8_UART3_RX
VDD1V8_UART3_TX
UART2_RX
UART2_TX
UART0_RX
UART0_TX
PWM4
PWM3
LDO5VCC_I2S_MCLK
LDO5VCC_I2S_SCLK
LDO5VCC_I2S_SDO
LDO5VCC_I2S_SDI
LDO5VCC_I2S_LRCLK
LDO5VCC_UART3_RX
LDO5VCC_UART3_TX
LDO5VCC_UART3_CTS
GND
LDO5VCC_SDIO_CLK
LDO5VCC_SDIO_CMD
LDO5VCC_SDIO_D3
LDO5VCC_SDIO_D2
LDO5VCC_SDIO_D1
LDO5VCC_SDIO_D0
VDD_LDO5
GND
PCIE_REFCLKN
PCIE_REFCLKP
GND
USB_OTG_SSRXN
USB_OTG_SSRXP
GND
USB_OTG_DP
USB_OTG_DM
GND
USB_OTG_SSTXN
USB_OTG_SSTXP
GND
USB_HOST_DP
USB_HOST_DM
GND
CAN0_RX
CAN0_TX
CAN1_RX
CAN1_TX
GND
I2C2_SDA
I2C2_SCL
I2C1_SDA
I2C1_SCL
SPI_MISO
SPI_MOSI
SPI_CS
SPI_CLK
GND
DD3
RK3562
USB
PCIE
USB30_OTG0_DP
USB30_OTG0_DM
USB30_OTG0_VBUSDET
USB30_OTG0_ID
USB20_HOST1_DP
USB20_HOST1_DM
USB30_OTG0_SSTXP/PCIE20_TXDP
USB30_OTG0_SSTXN/PCIE20_TXDN
USB30_OTG0_SSRXP/PCIE20_RXDP
USB30_OTG0_SSRXN/PCIE20_RXDN
PCIE20_REFCLKP
PCIE20_REFCLKN
MULTI_PHY_AVDD_0V9
MULTI_PHY_AVDD_1V8
USB_AVDD_0V9
USB_AVDD_1V8
USB_AVDD_3V3
RK3562J
USB_OTG_DP
USB_OTG_DM
USB_HOST_DP
USB_HOST_DM
USB_OTG_SSTXP
USB_OTG_SSTXN
USB_OTG_SSRXP
USB_OTG_SSRXN
PCIE_REFCLKP
PCIE_REFCLKN
VDDA_0V9
VDDA_1V8
VDDA_0V9
VDDA_1V8
VDD_3V3
R35
1.5k
USB_OTG_VBUS
R36
2.49k
GND
R34
100
USB_OTG_ID
DD3
RK3562
MIPI CSI0
MIPI CSI1
MIPI_CSI_RX0_D0P
MIPI_CSI_RX0_D0N
MIPI_CSI_RX0_D1P
MIPI_CSI_RX0_D1N
MIPI_CSI_RX0_D2P
MIPI_CSI_RX0_D2N
MIPI_CSI_RX0_D3P
MIPI_CSI_RX0_D3N
MIPI_CSI_RX0_CLK0P
MIPI_CSI_RX0_CLK0N
MIPI_CSI_RX0_CLK1P
MIPI_CSI_RX0_CLK1N
MIPI_CSI_RX1_D0P
MIPI_CSI_RX1_D0N
MIPI_CSI_RX1_D1P
MIPI_CSI_RX1_D1N
MIPI_CSI_RX1_D2P
MIPI_CSI_RX1_D2N
MIPI_CSI_RX1_D3P
MIPI_CSI_RX1_D3N
MIPI_CSI_RX1_CLK0P
MIPI_CSI_RX1_CLK0N
MIPI_CSI_RX1_CLK1P
MIPI_CSI_RX1_CLK1N
MIPI_CSI_RX0_AVDD_0V9
MIPI_CSI_RX0_AVDD_1V8
MIPI_CSI_RX1_AVDD_0V9
MIPI_CSI_RX1_AVDD_1V8
RK3562J
MIPI_CSI_RXD0P
MIPI_CSI_RXD0N
MIPI_CSI_RXD1P
MIPI_CSI_RXD1N
MIPI_CSI_RXD2P
MIPI_CSI_RXD2N
MIPI_CSI_RXD3P
MIPI_CSI_RXD3N
MIPI_CSI_CLK0P
MIPI_CSI_CLK0N
MIPI_CSI_CLK1P
MIPI_CSI_CLK1N
VDDA_0V9
VDDA_1V8
VDDA_0V9
VDDA_1V8
DD3
RK3562
MIPI
LVDS
MIPI_DSI_TX_D0P/LVDS_TX_D0P
MIPI_DSI_TX_D0N/LVDS_TX_D0N
MIPI_DSI_TX_D1P/LVDS_TX_D1P
MIPI_DSI_TX_D1N/LVDS_TX_D1N
MIPI_DSI_TX_D2P/LVDS_TX_D2P
MIPI_DSI_TX_D2N/LVDS_TX_D2N
MIPI_DSI_TX_D3P/LVDS_TX_D3P
MIPI_DSI_TX_D3N/LVDS_TX_D3N
MIPI_DSI_TX_CLKP/LVDS_TX_CLKP
MIPI_DSI_TX_CLKN/LVDS_TX_CLKN
MIPI_DSI_TX/LVDS_TX_AVDD_0V9
MIPI_DSI_TX/LVDS_TX_AVDD_1V8
RK3562J
MIPI_LVDS_TXD0P
MIPI_LVDS_TXD0N
MIPI_LVDS_TXD1P
MIPI_LVDS_TXD1N
MIPI_LVDS_TXD2P
MIPI_LVDS_TXD2N
MIPI_LVDS_TXD3P
MIPI_LVDS_TXD3N
MIPI_LVDS_CLKP
MIPI_LVDS_CLKN
VDDA_0V9
VDDA_1V8
DD3
RK3562
SRADC0
SRADC1
OTP
SARADC0_BOOT
SARADC0_IN1
SARADC0_IN2
SARADC0_IN3
SARADC0_IN4
SARADC0_IN5
SARADC0_IN6
SARADC0_IN7
SARADC1_IN0
SARADC1_IN1
SARADC1_IN2
SARADC1_IN3
SARADC1_IN4
SARADC1_IN5
SARADC1_IN6
SARADC1_IN7
SARADC0_AVDD_1V8
SARADC1_AVDD_1V8
OTP_VCC_1V8
RK3562J
BOOT
RECOVERY
ADC_IN4_HP_HOOK
ADC_IN5
VDDA_1V8
VDDA_1V8
VDDA_1V8
DD3
RK3562
PMUIO0
TSADC_SHUT_M0/TSADC_SHUT_ORG/GPIO0_A1_z
REF_CLK_OUT/GPIO0_A0_d
PWR_CTRL0/TSADC_SHUT_M1/GPIO0_A2_d
PWR_CTRL1/GPIO0_A3_u
SDMMC0_DETN/I2C4_SDA_M1/GPIO0_A4_u
SDMMC0_PWREN/I2C4_SCL_M1/PMU_DEBUG/GPIO0_A5_d
PCIE20_CLKREQN_M0/GPIO0_A6_d
PWM3_M0/GPIO0_A7_d
CLK_32K_IN/CLK0_32K_OUT/PCIE20_BUTTONRSTN/GPIO0_B0_d
I2C0_SCL/GPIO0_B1_d
I2C0_SDA/GPIO0_B2_d
nPOR
PMUIO0
R37
0
PMIC_SLEEP
PMIC_INT
GPIO0_A4
RTC_INT
PWM3
PMIC_SCL
PMIC_SDA
RESET
LDO_3V3_PMU
PMUIO1
I2C1_SCL_M0/GPIO0_B3_d
I2C1_SDA_M0/GPIO0_B4_d
I2C2_SCL_M0/PCIE20_PERSTN_M0/GPIO0_B5_d
I2C2_SDA_M0/PCIE20_WAKEN_M0/GPIO0_B6_d
SPI0_CSN1_M0/PWM4_M0/CPU_AVS/SPDIF_TX_M1/GPIO0_B7_d
UART2_TX_M0/PWM7_M0/GPU_AVS/CAN1_TX_M1/GPIO0_C0_d
UART2_RX_M0/PWM6_M0/CAN1_RX_M1/NPU_AVS/GPIO0_C1_d
UART2_CTSN_M0/PWM5_M0/SPI0_CSN0_M0/GPIO0_C2_d
UART2_RTSN_M0/PWM0_M0/SPI0_CLK_M0/GPIO0_C3_d
UART6_CTSN_M0/PWM1_M0/SPI0_MOSI_M0/GPIO0_C4_d
UART6_RTSN_M0/PWM2_M0/SPI0_MISO_M0/GPIO0_C5_d
UART6_TX_M0/CAN0_TX_M2/GPIO0_C6_d
UART6_RX_M0/CAN0_RX_M2/GPIO0_C7_d
UART0_RX_M0/JTAG_CPU_MCU_TMS_M0/GPIO0_D0_u
UART0_TX_M0/JTAG_CPU_MCU_TCK_M0/GPIO0_D1_u
PMUIO1
I2C1_SCL
I2C1_SDA
I2C2_SCL
I2C2_SDA
PWM4
CAN1_TX
CAN1_RX
SPI_CS
SPI_CLK
SPI_MOSI
SPI_MISO
CAN0_TX
CAN0_RX
UART0_RX
UART0_TX
VDD_3V3
VDDA_0V9_PMU
VDDA_0V9_PMU
VDDA_0V9
LDO_1V8
VDDA_1V8
GND
GND
PMU_VDD_LOGIC_0V9
PMUPLL_AVDD_0V9
SYSPLL_AVDD_0V9
PMUPLL_AVDD_1V8
SYSPLL_AVDD_1V8
TVSS
PMUPLL_AVSS
OSC_SOC_XOUT
OSC_SOC_XIN
RK3562J
C59
22p
ZQ2
GND
GND
24M
C60
22p
R23
2.2M
R24
0
DD3
RK3562
VCCIO1
I2C3_SCL_M0/UART2_TX_M1/PDM_SDI3_M0/CAN0_TX_M0/UART5_CTSN_M1/GPIO3_A0_d
I2C3_SDA_M0/UART2_RX_M1/SPDIF_TX_M0/CAN0_RX_M0/UART5_RTSN_M1/GPIO3_A1_d
I2S0_MCLK_M0/UART2_CTSN_M1/PDM_CLK1_M0/GPIO3_A2_d
I2S0_SCLK_M0/UART2_RTSN_M1/GPIO3_A3_d
I2S0_LRCK_M0/PWM8_M0/GPIO3_A4_d
I2S0_SDO0_M0/PWM9_M0/GPIO3_A5_d
I2S0_SDO1_M0/I2S0_SDI3_M0/PDM_CLK0_M0/PCIE20_CLKREQN_M1/UART5_TX_M1/GPIO3_A6_d
I2S0_SDO2_M0/I2S0_SDI2_M0/PDM_SDI2_M0/PCIE20_WAKEN_M1/UART5_RX_M1/GPIO3_A7_d
I2S0_SDO3_M0/I2S0_SDI1_M0/PDM_SDI1_M0/PCIE20_PERSTN_M1/GPIO3_B0_d
I2S0_SDI0_M0/PDM_SDI0_M0/GPIO3_B1_d
RK3562J
VCCIO1
UART2_TX
UART2_RX
PMIC_MCLK
PMIC_SCLK
PMIC_LRCK
PMIC_SDO
PMIC_PDMCLK
PMIC_SDI
LDO_3V3
J1
Boot sel.
GND
SW1
GND
BOOT
R18
5.1k
VDDA_1V8
VD2
GND
C57
0.1u
GND
R21
5.1k
VDDA_1V8
RECOVERY
DD3
RK3562 VCCIO3
SDMMC0_D0/UART0_RX_M1/UART7_RX_M1/SPI1_MOSI_M1/DSM_AUD_LP/GPIO1_B3_u
SDMMC0_D1/UART0_TX_M1/UART7_TX_M1/SPI1_MISO_M1/DSM_AUD_LN/GPIO1_B4_u
SDMMC0_D2/JTAG_CPU_MCU_TCK_M1/UART5_CTSN_M0/SPI1_CSN1_M1/PWM10_M0/DSM_AUD_RP/GPIO1_B5_u
SDMMC0_D3/JTAG_CPU_MCU_TMS_M1/UART5_RTSN_M0/SPI1_CSN0_M1/PWM11_M0/DSM_AUD_RN/GPIO1_B6_u
SDMMC0_CMD/SPDIF_TX_M2/UART5_RX_M0/CAN1_RX_M0/GPIO1_B7_u
SDMMC0_CLK/TEST_CLK_OUT/UART5_TX_M0/SPI1_CLK_M1/CAN1_TX_M0/GPIO1_C0_d
RK3562J
VCCIO3
LDO5VCC_SDIO_D0
LDO5VCC_SDIO_D1
LDO5VCC_SDIO_D2
LDO5VCC_SDIO_D3
LDO5VCC_SDIO_CMD
LDO5VCC_SDIO_CLK
VDD_LDO5
GND
C58
DNP
ZQ1
32768
VDD_3V3
R10
DNP
R14
0
LDO_3V3_PMU
VD1
VDD_RTC
VBAT
RTC_INT
GND
X1
X2
INT
Gnd
RTC
DD5
PCF8563TS/4
Vcc
C/O
SCL
SDA
GND
C45
0.1u
VDD_RTC
PMIC_SCL
PMIC_SDA
R32
5.1k
LDO_3V3_PMU
RTC_INT
DD3
RK3562 VCCIO5
I2S1_MCLK_M1/CAM_CLK0_OUT_M0/UART8_TX_M0/GPIO3_B2_d
I2S1_SCLK_M1/CAM_CLK1_OUT_M0/UART8_RX_M0/GPIO3_B3_d
I2S1_LRCK_M1/CAM_CLK2_OUT/UART8_CTSN_M0/SPI0_MOSI_M1/PWM12_M1/GPIO3_B4_d
I2S1_SDO0_M1/CAM_CLK3_OUT/UART8_RTSN_M0/SPI0_CLK_M1/PWM13_M1/GPIO3_B5_d
I2S1_SDI1_M1/I2S1_SDI3_M1/UART3_CTSN_M1/SPI0_CSN1_M1/I2C4_SCL_M0/CAN0_TX_M1/GPIO3_B6_d
I2S1_SDO2_M1/I2S1_SDI2_M1/UART3_TX_M1/SPI0_CSN0_M1/I2C4_SDA_M0/CAN0_RX_M1/GPIO3_B7_d
I2S1_SDO3_M1/I2S1_SDI1_M1/UART3_RX_M1/SPI0_MISO_M1/GPIO3_C0_d
I2S1_SDI0_M1/ISP_FLASH_TRIGIN/UART3_RTSN_M1/GPIO3_C1_d
I2C5_SCL_M0/ISP_PRELIGHT_TRIGOUT/UART9_TX_M1/GPIO3_C2_d
I2C5_SDA_M0/ISP_FLASH_TRIGOUT/UART9_RX_M1/GPIO3_C3_d
RK3562J
VCCIO5
LDO5VCC_I2S_MCLK
LDO5VCC_I2S_SCLK
LDO5VCC_I2S_LRCLK
LDO5VCC_I2S_SDO
LDO5VCC_UART3_CTS
LDO5VCC_UART3_TX
LDO5VCC_UART3_RX
LDO5VCC_I2S_SDI
LDO5VCC_GPIO3_C2
LDO5VCC_GPIO3_C3
VDD_LDO5
SK-RK3562-MOD
www.starterkit.ru
V1.A


DD3
RK3562
VCCIO4
SDMMC1_D0/RGMII_TXD2_M1/I2S0_SDI0_M1/PWM8_M1/GPIO1_C1_d
SDMMC1_D1/RGMII_TXD3_M1/I2S0_SDI1_M1/PWM9_M1/GPIO1_C2_d
SDMMC1_D2/RGMII_TXCLK_M1/I2S0_SDO0_M1/PWM10_M1/GPIO1_C3_d
SDMMC1_D3/RGMII_RXD2_M1/I2S0_LRCK_M1/PWM11_M1/GPIO1_C4_d
SDMMC1_CMD/RGMII_RXD3_M1/I2S0_SCLK_M1/PWM0_M1/GPIO1_C5_d
SDMMC1_CLK/RGMII_RXCLK_M1/I2S0_MCLK_M1/PWM1_M1/GPIO1_C6_d
SDMMC1_PWREN/RGMII_MDC_M1/PWM2_M1/I2C5_SCL_M1/RMII_MDC/GPIO1_C7_d
SDMMC1_DETN/RGMII_MDIO_M1/PWM3_M1/I2C5_SDA_M1/RMII_MDIO/GPIO1_D0_d
UART1_RX_M0/RGMII_TXD0_M1/I2S0_SDO1_M1/PWM4_M1/RMII_TXD0/GPIO1_D1_d
UART1_TX_M0/RGMII_TXD1_M1/I2S0_SDO2_M1/PWM5_M1/RMII_TXD1/GPIO1_D2_d
UART1_RTSN_M0/RGMII_TXEN_M1/I2S0_SDI2_M1/PWM6_M1/RMII_TXEN/GPIO1_D3_d
UART1_CTSN_M0/RGMII_RXD0_M1/I2S0_SDI3_M1/PWM7_M1/RMII_RXD0/GPIO1_D4_d
I2S2_SCLK_M0/RGMII_CLK_M1/UART4_RX_M1/SPI2_CSN1_M1/RMII_TXCK/GPIO1_D5_d
I2S2_LRCK_M0/RGMII_RXDV_M1/UART4_TX_M1/SPI2_CSN0_M1/RMII_RXDV_CRS/GPIO1_D6_d
I2S2_SDO_M0/RGMII_RXD1_M1/UART4_RTSN_M1/SPI2_MOSI_M1/PWM14_M1/RMII_RXD1/GPIO1_D7_d
I2S2_SDI_M0/RGMII_RXER_M1/UART4_CTSN_M1/SPI2_MISO_M1/PWM15_M1/RMII_RXER/GPIO2_A0_d
I2S2_MCLK_M0/ETH_CLK_25M_OUT_M1/CLK1_32K_OUT/SPI2_CLK_M1/I2S0_SDO3_M1/GPIO2_A1_d
RK3562J
VCCIO4
VDD_3V3
RGMII_RESET
RMII_RESET
RMII_MDC
RMII_MDIO
RMII_TX0
RMII_TX1
RMII_TXEN
RMII_RX0
RMII_RX1
RMII_TXCK
RMII_RXER
RMII_CRS
RMII_RX0
RMII_RX1
RMII_RXDV
RMII_TXCK
RMII_MDC
RMII_MDIO
RMII_25MHZ
DD6
PHY
10/100M
TXD0
TXD1
TXD2
TXD3
TXEN
RXD0
RXD1
RXD2
RXD3
COL/RMII
RXER
RXDV/CRS_DV
CRS/LEDmod
TXCLK/50MClki
RXCLK/50MClko
TXER/FXSD
MDC
MDIO
XI
XO
MDIRP
MDIRN
MDITP
MDITN
TESTON
nRST
LED0/ADO
LED3/AD3
ISET
REGOUT
IOVDD
AVDD33
PGND
ETH2_RXP
ETH2_RXN
ETH2_TXP
ETH2_TXN
RMII_RESET
R16 1.5k
R17 5.1k
R11 DNP
R15 5.1k
R12 6.19k
R19 5.1k
R20 5.1k
C47 10u
C46 0.1u
C48 0.1u
VDD_3V3
GND
DD3
RK3562
VCCIO6
VQ_LCDC_D3/I2S1_MCLK_M0/UART7_TX_M0/GPIO3_C4_d
VQ_LCDC_D4/I2S1_SCLK_M0/UART4_CTSN_M0/PWM14_M0/GPIO3_C5_d
VQ_LCDC_D5/I2S1_LRCK_M0/UART4_RTSN_M0/PWM15_M0/GPIO3_C6_d
VQ_LCDC_D6/I2S1_SDO0_M0/UART7_RX_M0/GPIO3_C7_d
VQ_LCDC_D7/I2S1_SDI0_M0/UART4_TX_M0/GPIO3_D0_d
VQ_LCDC_D10/I2S1_SDI1_M0/UART4_RX_M0/UART3_RTSN_M0/GPIO3_D1_d
VQ_LCDC_D11/I2S1_SDI2_M0/UART7_CTSN_M0/SPI2_MISO_M0/I2C2_SCL_M1/GPIO3_D2_d
VQ_LCDC_D12/I2S1_SDI3_M0/UART7_RTSN_M0/SPI2_MOSI_M0/I2C2_SDA_M1/GPIO3_D3_d
VQ_LCDC_D13/RGMII_TXD2_M0/UART8_TX_M1/I2S2_SDI_M1/GPIO3_D4_d
VQ_LCDC_D14/RGMII_TXD3_M0/UART8_RX_M1/I2S2_SDO_M1/GPIO3_D5_d
VQ_LCDC_D15/RGMII_TXCLK_M0/I2S2_MCLK_M1/SPI1_CLK_M0/GPIO3_D6_d
VQ_LCDC_D19/RGMII_RXD2_M0/UART8_CTSN_M1/SPI1_CSN0_M0/GPIO3_D7_d
VQ_LCDC_D20/RGMII_RXD3_M0/UART8_RTSN_M1/SPI1_CSN1_M0/GPIO4_A0_d
VQ_LCDC_D21/RGMII_RXCLK_M0/I2S2_LRCK_M1/PWM12_M0/GPIO4_A1_d
VQ_LCDC_D22/RGMII_TXD0_M0/UART6_CTSN_M1/SPI1_MOSI_M0/GPIO4_A2_d
VQ_LCDC_D23/RGMII_TXD1_M0/UART6_RTSN_M1/SPI1_MISO_M0/GPIO4_A3_d
VQ_LCDC_D0/RGMII_TXEN_M0/PWM13_M0/GPIO4_A4_d
VQ_LCDC_D1/RGMII_RXD0_M0/UART1_TX_M1/PDM_SDI2_M1/I2C3_SCL_M1/GPIO4_A5_d
VQ_LCDC_D8/RGMII_RXD1_M0/UART1_RX_M1/PDM_SDI3_M1/I2C3_SDA_M1/GPIO4_A6_d
VQ_LCDC_D9/RGMII_RXDV_M0/UART1_RTSN_M1/PDM_SDI0_M1/UART6_TX_M1/GPIO4_A7_d
VQ_LCDC_D16/RGMII_RXER_M0/UART1_CTSN_M1/PDM_SDI1_M1/UART6_RX_M1/GPIO4_B0_d
VQ_LCDC_D17/ETH_CLK_25M_OUT_M0/CAM_CLKO_OUT_M1/I2S2_SCLK_M1/PDM_CLK1_M1/GPIO4_B1_d
VQ_LCDC_D2/RGMII_MDC_M0/UART9_TX_M0/GPIO4_B2_d
VQ_LCDC_D18/RGMII_MDIO_M0/UART9_RX_M0/GPIO4_B3_d
VQ_LCDC_HSYNC/I2S1_SDO1_M0/UART9_CTSN_M0/SPI2_CSN1_M0/I2C1_SCL_M1/UART3_TX_M0/GPIO4_B4_d
VQ_LCDC_VSYNC/I2S1_SDO2_M0/UART9_RTSN_M0/SPI2_CSN0_M0/I2C1_SDA_M1/UART3_RX_M0/GPIO4_B5_d
VQ_LCDC_DEN/I2S1_SDO3_M0/UART3_CTSN_M0/SPI2_CLK_M0/GPIO4_B6_d
VQ_LCDC_CLK/RGMII_CLK_M0/CAM_CLK1_OUT_M1/PDM_CLK0_M1/GPIO4_B7_d
RK3562J
VCCIO6_1
VCCIO6_2
VDD_1V8
VDD1V8_UART4_TX
VDD1V8_UART4_RX
RGMII_TXD2
RGMII_TXD3
RGMII_TXC
RGMII_RXD2
RGMII_RXD3
RGMII_RXC
RGMII_TXD0
RGMII_TXD1
RGMII_TXCTL
RGMII_RXD0
RGMII_RXD1
RGMII_RXCTL
RGMII_25MHz
RGMII_MDC
RGMII_MDIO
VDD1V8_UART3_TX
VDD1V8_UART3_RX
RGMII_125MHz
DD1
10/100/1000M
Ethernet PHY
TXD0
TXD1
TXD2
TXD3
TXCTL
TXC
RXD0/RXDLY
RXD1/RXDLY
RXD2/PLLOFF
RXD3/PHYAD0
RXCTL/PHYAD2
RXC/PHYAD1
MDC
MDIO
PHYRSTB
CLKOUT
INTB/PMEB
LED0/CFG_EXT
LED1/CFG_LDO0
LED2/CFG_LDO1
XTAL_IN
XTAL_OUT/EXT_CLK
RSET
MDIOP
MDION
MDI1P
MDI1N
MDI2P
MDI2N
MDI3P
MDI3N
DVDD33
DVDD_RG
AVDD33_1
AVDD33_2
DVDD10
AVDD10_1
AVDD10_2
AVDD10_3
GND
REG_OUT
L1 2.2u
ETH1_TRD1P
ETH1_TRD1N
ETH1_TRD2P
ETH1_TRD2N
ETH1_TRD3P
ETH1_TRD3N
ETH1_TRD4P
ETH1_TRD4N
R67 DNP
R33 5.1k
R50 5.1k
R62 5.1k
R63 5.1k
R49 5.1k
R61 DNP
R53 5.1k
R60 DNP
R52 5.1k
R54 100
R64 5.1k
R68 5.1k
R69 5.1k
C143 0.1u
R51 100
R71 2.49k
PHY_CFG_EXT
PHY_CFG_LDO0
PHY_CFG_LDO1
C154 C148 C144 C152 C146 C149 C153 C1
0.1u 0.1u 0.1u 0.1u 0.1u 0.1u 0.1u 10u
DD3
RK3562
LPDDR4
LPDDR4_DQ0_A
LPDDR4_DQ1_A
LPDDR4_DQ2_A
LPDDR4_DQ3_A
LPDDR4_DQ4_A
LPDDR4_DQ5_A
LPDDR4_DQ6_A
LPDDR4_DQ7_A
LPDDR4_DQ8_A
LPDDR4_DQ9_A
LPDDR4_DQ10_A
LPDDR4_DQ11_A
LPDDR4_DQ12_A
LPDDR4_DQ13_A
LPDDR4_DQ14_A
LPDDR4_DQ15_A
LPDDR4_DQ0_B
LPDDR4_DQ1_B
LPDDR4_DQ2_B
LPDDR4_DQ3_B
LPDDR4_DQ4_B
LPDDR4_DQ5_B
LPDDR4_DQ6_B
LPDDR4_DQ7_B
LPDDR4_DQ8_B
LPDDR4_DQ9_B
LPDDR4_DQ10_B
LPDDR4_DQ11_B
LPDDR4_DQ12_B
LPDDR4_DQ13_B
LPDDR4_DQ14_B
LPDDR4_DQ15_B
LPDDR4_DQS0P_A
LPDDR4_DQS0N_A
LPDDR4_DQS1P_A
LPDDR4_DQS1N_A
LPDDR4_DQS0P_B
LPDDR4_DQS0N_B
LPDDR4_DQS1P_B
LPDDR4_DQS1N_B
LPDDR4_DM0_A
LPDDR4_DM1_A
LPDDR4_DM0_B
LPDDR4_DM1_B
LPDDR4_A0_A
LPDDR4_A1_A
LPDDR4_A2_A
LPDDR4_A3_A
LPDDR4_A4_A
LPDDR4_A5_A
LPDDR4_A0_B
LPDDR4_A1_B
LPDDR4_A2_B
LPDDR4_A3_B
LPDDR4_A4_B
LPDDR4_A5_B
LPDDR4_CLKP_A
LPDDR4_CLKN_A
LPDDR4_CLKP_B
LPDDR4_CLKN_B
LPDDR4_CSON_A
LPDDR4_CSIN_A
LPDDR4_ODT0_A
LPDDR4_ODT1_A
LPDDR4_CKE0_A
LPDDR4_CKE1_A
LPDDR4_CSON_B
LPDDR4_CSIN_B
LPDDR4_ODT0_B
LPDDR4_ODT1_B
LPDDR4_CKE0_B
LPDDR4_CKE1_B
LPDDR4_RESETN
DDR_RZQ
DDR_VREFOUT
DDR_NC
DDR_VDDQ_1
DDR_VDDQ_2
DDR_VDDQ_3
DDR_VDDQ_4
DDR_VDDQ_5
DDR_VDDQ_6
DDR_VDDQL_1
DDR_VDDQL_2
DDR_VDDQL_3
DDR_VDDQL_4
RK3562J
DDR_A0_A
DDR_A1_A
DDR_A2_A
DDR_A3_A
DDR_A4_A
DDR_A5_A
DDR_A0_B
DDR_A1_B
DDR_A2_B
DDR_A3_B
DDR_A4_B
DDR_A5_B
DDR_CLKP_A
DDR_CLKN_A
DDR_CLKP_B
DDR_CLKN_B
DDR_CS0_A
DDR_CS1_A
DDR_CS0_B
DDR_CS1_B
DDR_CKE0_B
DDR_CKE1_B
DDR_RESET
CPU_RZQ
VDD_DDR
R39 240
R40 240
DD4
PDDR4
DDR_DQ0_A
DDR_DQ1_A
DDR_DQ2_A
DDR_DQ3_A
DDR_DQ4_A
DDR_DQ5_A
DDR_DQ6_A
DDR_DQ7_A
DDR_DQ8_A
DDR_DQ9_A
DDR_DQ10_A
DDR_DQ11_A
DDR_DQ12_A
DDR_DQ13_A
DDR_DQ14_A
DDR_DQ15_A
DDR_DM0_A
DDR_DM1_A
DDR_DQS0P_A
DDR_DQS0N_A
DDR_DQS1P_A
DDR_DQS1N_A
DDR_CS0_A
DDR_CKE0_A
DDR_CK_t_A
DDR_CK_c_A
DDR_CA0_A
DDR_CA1_A
DDR_CA2_A
DDR_CA3_A
DDR_CA4_A
DDR_CA5_A
DDR_CS1_A
DDR_CKE1_A
ODT_CA_A
DDR_DQ0_B
DDR_DQ1_B
DDR_DQ2_B
DDR_DQ3_B
DDR_DQ4_B
DDR_DQ5_B
DDR_DQ6_B
DDR_DQ7_B
DDR_DQ8_B
DDR_DQ9_B
DDR_DQ10_B
DDR_DQ11_B
DDR_DQ12_B
DDR_DQ13_B
DDR_DQ14_B
DDR_DQ15_B
DDR_DM0_B
DDR_DM1_B
DDR_DQS0P_B
DDR_DQS0N_B
DDR_DQS1P_B
DDR_DQS1N_B
DDR_CS0_B
DDR_CKE0_B
DDR_CK_t_B
DDR_CK_c_B
DDR_CA0_B
DDR_CA1_B
DDR_CA2_B
DDR_CA3_B
DDR_CA4_B
DDR_CA5_B
DDR_CS1_B
DDR_CKE1_B
ODT_CA_B
RESET
ZQ0
ZQ1
DDR_RESET
DDR_RZQ0
DDR_RZQ1
VDD_DDR
VDD_0V6
VDD_1V8
VSS_45
VSS_46
VSS_47
VSS_48
VSS_49
VSS_54
VSS_53
VSS_52
VSS_51
VSS_50
VSS_35
VSS_36
VSS_37
VSS_38
VSS_39
VSS_44
VSS_43
VSS_42
VSS_41
VSS_40
VSS_34
VSS_33
VSS_55
VSS_56
VSS_21
VSS_22
VSS_23
VSS_24
VSS_25
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VSS_26
VSS_11
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VSS_20
VSS_19
VSS_18
VSS_58
VSS_17
VSS_16
VSS_10
VSS_9
VSS_31
VSS_32
VSS_3
VSS_4
VSS_5
VSS_6
VSS_7
VSS_8
VSS_2
VSS_1
VSS_0
GND
DD3
RK3562
VCCIO2
EMMC_D0/FSPI_D0/GPIO1_A0_u
EMMC_D1/FSPI_D1/GPIO1_A1_u
EMMC_D2/FSPI_D2/GPIO1_A2_u
EMMC_D3/FSPI_D3/GPIO1_A3_u
EMMC_D4/GPIO1_A4_u
EMMC_D5/GPIO1_A5_u
EMMC_D6/GPIO1_A6_u
EMMC_D7/GPIO1_A7_u
EMMC_CMD/FSPI_CSN0/GPIO1_B0_u
EMMC_CLK/FSPI_CLK/GPIO1_B1_d
EMMC_STRB/FSPI_CSN1/GPIO1_B2_d
RK3562J
VCCIO2
VDD_1V8
EMMC_D0
EMMC_D1
EMMC_D2
EMMC_D3
EMMC_D4
EMMC_D5
EMMC_D6
EMMC_D7
EMMC_CMD
EMMC_CLK
EMMC_DS
DD7
eMMC
Flash
VDDF1
VDDF2
VDDF3
VDDF4
VDD1
VDD2
VDD3
VDD4
VDD5
VSSF1
VSSF2
VSSF3
VSSF4
VSS1
VSS2
VSS3
VSS4
VSS5
D0
D1
D2
D3
D4
D5
D6
D7
CLK
CMD
RES
DS
VDDI
VSS7
VSS6
R6 5.1k
R3 5.1k
R5 0
R13 5.1k
R4 DNP
C49 2.2u
C55 C53
0.1u 0.1u
VDD_3V3
GND
DDR_RZQ0
DDR_RZQ1
R55 240
R56 240